

IN THE CLAIMS:

Please delete the paragraph heading on page 14 of the English translation of the subject application, line 1, and insert in place thereof the paragraph heading as follows:

--CLAIMS--

Please insert the paragraph heading on page 14 of the English translation of the subject application, before claim 1, the following:

-- What is claimed is: --.

Please amend claims 1-12 as follows:

1. (Amended) A PCI bus interface circuit for the voltage supply of a PCT plug-in card that can be connected to a PCI bus, having:
- a first input for connection to a main voltage supply line of the PCI bus;
  - a second input for connection to an auxiliary voltage supply line of the PCI bus;
  - an output for outputting a supply voltage to the PCI plug-in card;
  - a first switching device for switching a main supply voltage that is present at the first input to the output if no auxiliary supply voltage  $V_{aux}$  is present at the second input;
  - a second switching device for switching an auxiliary supply voltage  $V_{aux}$  that is present at the second input to the output if no main supply voltage  $V_{cc}$  is present at the first input; and having
  - a third switching device, which, given the simultaneous presence of a main supply voltage  $V_{cc}$  at the first input and an auxiliary supply voltage  $V_{aux}$  at the second input, drives the second switching device for switching the auxiliary supply voltage  $V_{aux}$  through to the output.
2. (Amended) The interface circuit as claimed in claim 1, wherein the switching devices are semiconductor switches.
3. (Amended) The interface circuit as claimed in claim 1, wherein the switching devices are transistors each having a control terminal.
4. (Amended) The interface circuit as claimed in claim 1, wherein the switching devices are transistors, the third switching device being constructed complementarily with respect to the first and second switching devices.
5. (Amended) The interface circuit as claimed in claim 3, wherein the control terminal of the first transistor is connected to the second input and the control terminal of the second transistor is connected to the first input.
6. (Amended) The interface circuit as claimed in claim 3, wherein the control terminal of the third transistor is connected to the second input, the third transistor, when an auxiliary supply voltage is applied to the second input, turning on and connecting the control terminal of the second transistor to a specific voltage potential, with the results that the auxiliary supply voltage is switched through to the output.